



Attorney Docket No. 0756-2288

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Shunpei YAMAZAKI

Serial No. 09/832,844

Filed: April 12, 2001

For: ELECTRO-OPTICAL DEVICE

) Group Art Unit: 2826

) Examiner: M. Tran

) CERTIFICATE OF MAILING

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**INFORMATION DISCLOSURE STATEMENT**

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Dear Sir:

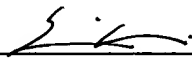
In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing information known to Applicant and requests that this information be made of record in the above identified application. Copies are submitted herewith in accordance with 37 C.F.R. 1.98(a).

The cited references were submitted to the Japanese Patent Office in connection with the features of "the analog switch" and "the CMOS transmission gate." In particular, the *Semiconductor Handbook*, Basic Circuit of Analog Switch, describes an analog switch circuit using the CMOS transmission gate shown in Fig. 11.65(d). The analog switch circuit of Fig. 11.65(d) drives the switch consisting of Tr3 and Tr4, by providing a pair of gate pulses each of which has opposite polarity to the other. The pair of gate pulses are provided by inputting the gate input pulse to the inverter circuit consisting of Tr1 and Tr2. English translations of the drawings of the reference are shown on the copy of the reference by manuscript.

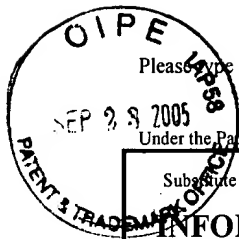
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The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 50-2280. A duplicate copy of this sheet is attached.

Respectfully submitted,

  
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				<b>Application Number</b>		09/832,844	
				<b>Filing Date</b>		April 12, 2001	
				<b>First Named Inventor</b>		Shunpei YAMAZAKI	
				<b>Group Art Unit</b>		2826	
				<b>Examiner Name</b>		M. Tran	
<b>Sheet</b>	1	<b>of</b>	1	<b>Attorney Docket Number</b>		0756-2288	

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Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				
		JP	64-010711			01/13/1989		Abst.
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		H. YANAI, 3.3.2 <i>Basic Circuit of Analog Switch</i> , Semiconductor Handbook, 2 <sup>nd</sup> Edition, Pages 842-843, 1977.	Concise Statement

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<sup>1</sup> Unique citation designation number. <sup>2</sup> See attached Kinds of U.S. Patent Documents. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

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